

**NAME**

**9tcfg** — Ninetails accelerator configuration program

**SYNOPSIS**

**9tcfg** [M68K/T] [M68KMEM/T] [PCMCIA/T] [MAPROM/T] [SHADOWROM/T] [LOADROM/K]  
[MOREMEM/S] [INSTCACHE/T] [REBOOT/S] [DEBUG/S] [HELP/S]

**DESCRIPTION**

The **9tcfg** command can be used to manage additional functions of Ninetails accelerator board. The **9tcfg** command takes the following flags and arguments:

**M68K/T**

68000 mode - disable/enable the 68020 processor after next reboot (off by default).

**M68KMEM/T**

68000 mode + Fast RAM - as above but with 5.5MB of Fast (off by default).

**INSTCACHE/T**

Turn off/on instruction cache (on by default).

**PCMCIA/T**

Enable PCMCIA access, configure only 4MB of RAM.

**MAPROM/T**

Enable/disable the MAPROM function, must be used with LOADROM/K (off by default).

**SHADOWROM/T**

Enable/disable the Shadow ROM function, may not be used when MAPROM is enabled (off by default).

**LOADROM/K**

Specify the Kickstart ROM file to load. Must be exactly 256kB, 512kB or 1MB.

**REBOOT/S**

Reboot the Amiga after setting requested options.

**MOREMEM/S**

Add 1,5MB of non-autoconfiguring Fast RAM to operating system memory list.

**DEBUG/S**

Print a lot of information useful only to developers.

**HELP/S**

Print help and exit.

**AUTHORS**

Ninetails board was designed and produced by Rafal Chyla. 9tcfg was written by Radoslaw "strim" Kujawa.